

<b>Notice of References Cited</b>	Application/Control No. 10/748,546		Applicant(s)/Patent Under Reexamination NUGENT, ALEX	
	Examiner Michael Caldwell		Art Unit 2129	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-3,222,654	12-1965	BERNARD WIDROW; et. al.	365/153
*	B	US-5,293,455	03-1994	Castelaz, Patrick F.	706/25
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Graf et al., "Advances In Neural Network Hardware," Electron Devices Meeting, 1988. Technical Digest., International 11-14 Dec. 1988 Pages: 766 - 769
	V	Gerousis et al., "Modeling Nanoelectronic CNN Cells: CMOS, SETS and QCAs," IEEE International Symposium on Circuits and Systems, May 28-31, 2000
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.